


4-9-2015

Power Efficient Circuits for Intrusion Detection using Memristor Crossbars

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Recommended Citation

"Power Efficient Circuits for Intrusion Detection using Memristor Crossbars" (2015). *Stander Symposium Posters*. 644.
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Power Efficient Circuits for Intrusion Detection using Memristor Crossbars

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Intrusion Detection System (IDS)

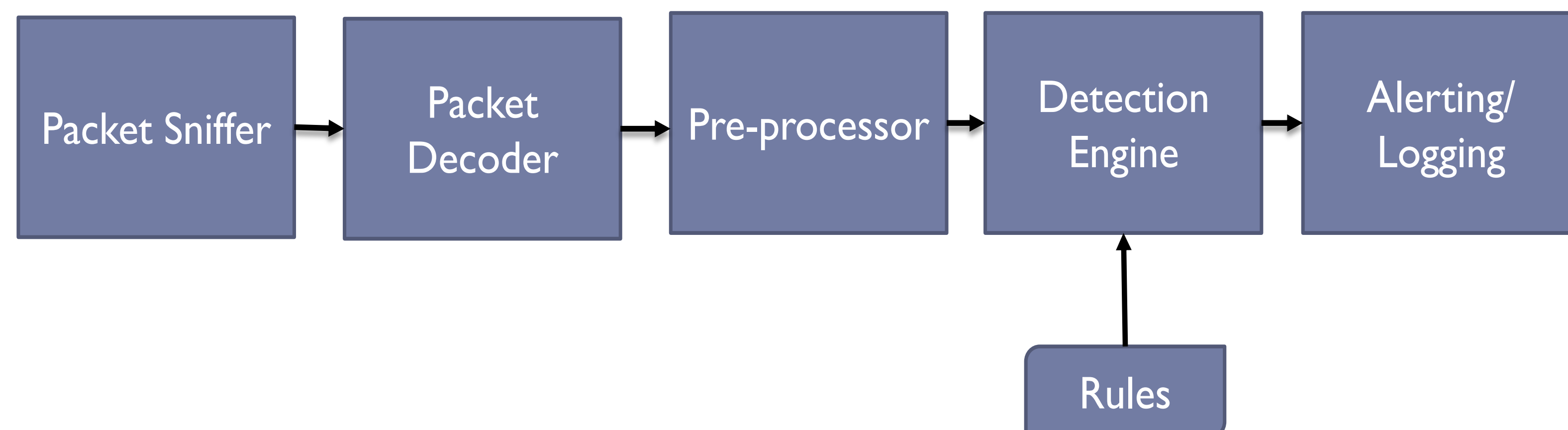
A device or software that offers cyber security
Discovery of invasions and attacks
Performing Deep Packet inspection techniques
Types of IDS based on target of monitoring
Host Based IDS
Network Based IDS
Types of IDS based on Detection
Signature Based IDS
Anomaly Based IDS

Snort

Powerful and popular network IDS
Free and open source software.
Supported by wide range of rules
Ability to perform Deep Packet inspection
Real time traffic analysis on networks.

Modes

- *Sniffer*
- *Packet Logger*
- *Network intrusion detection*

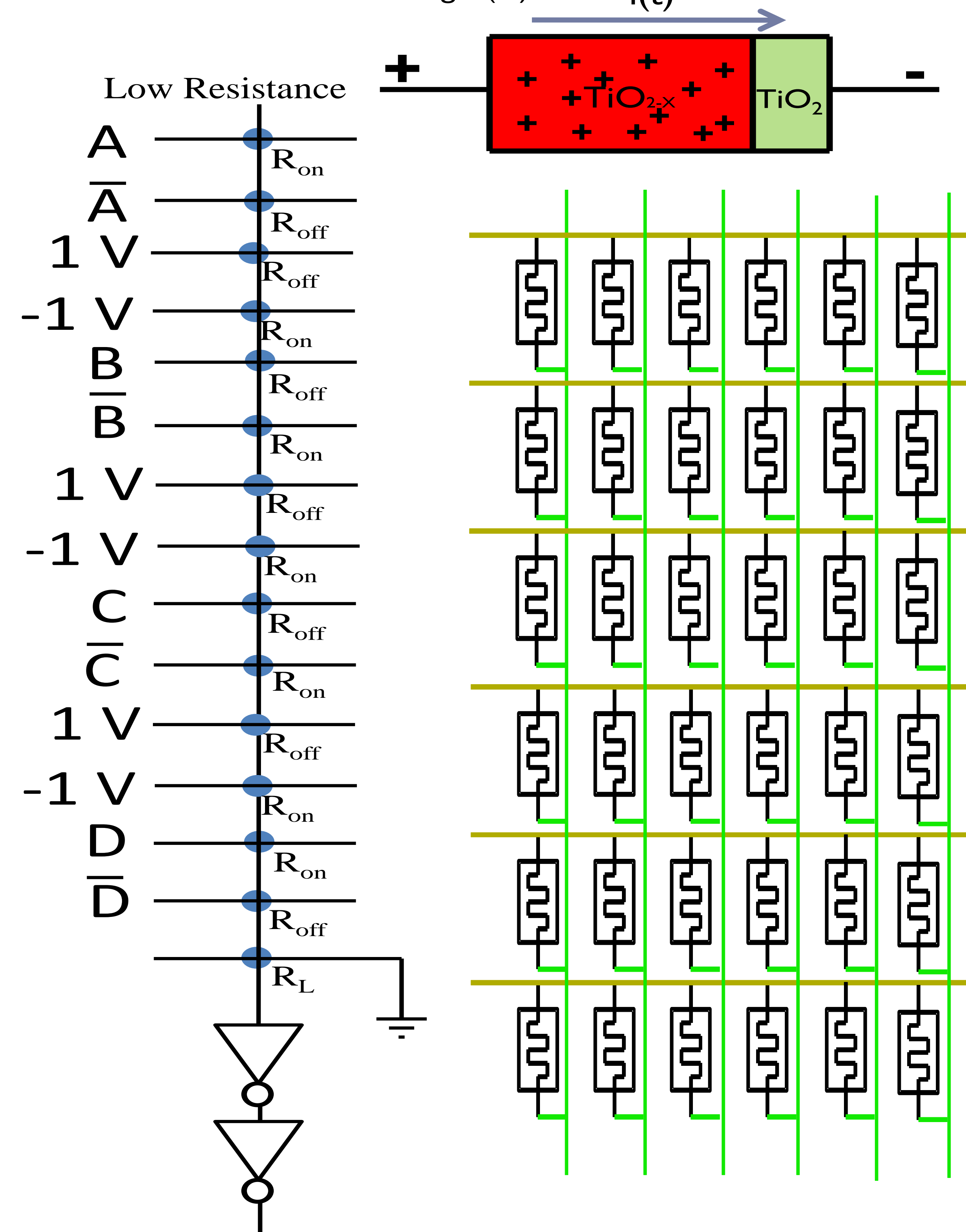
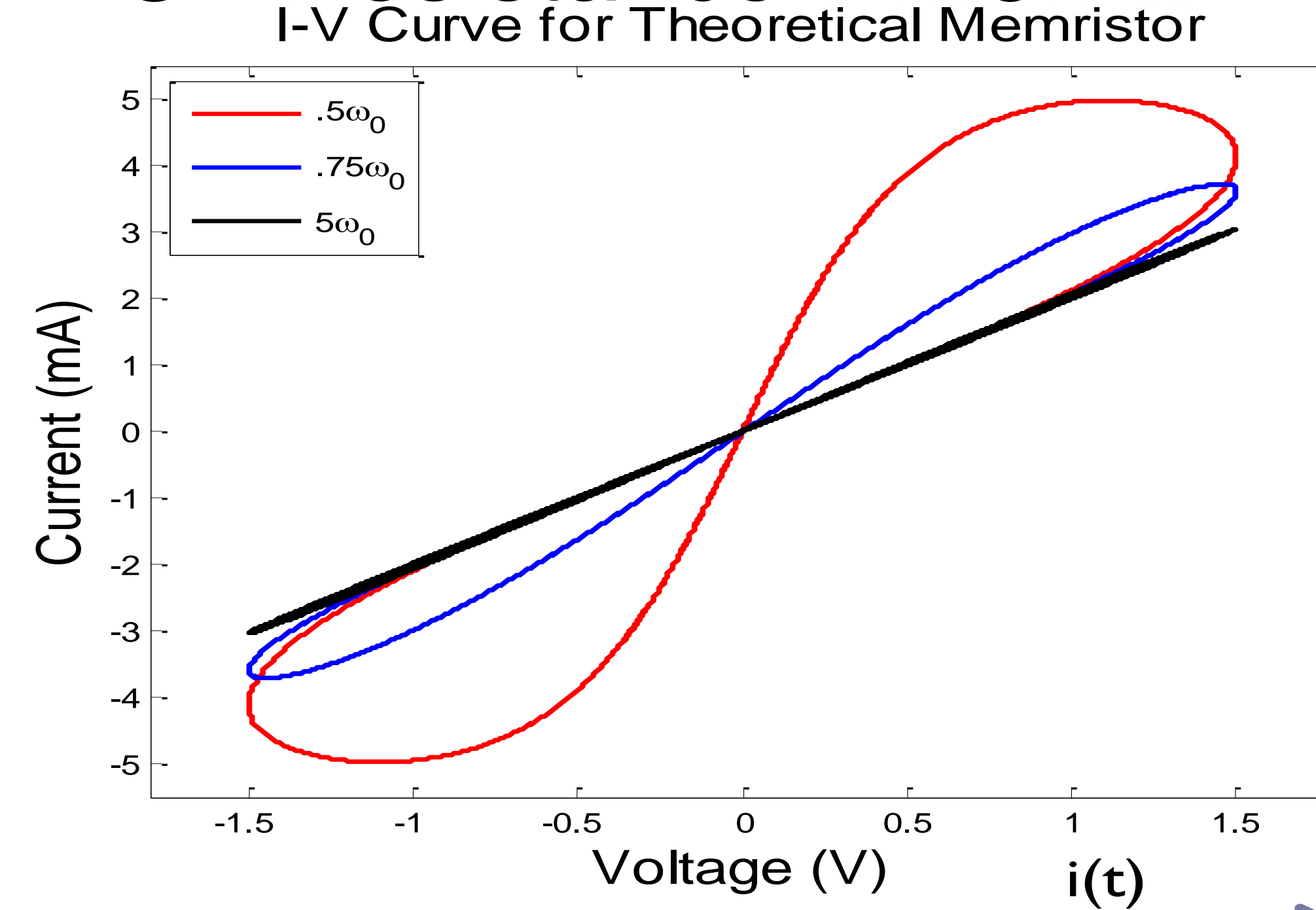


Snort rule contain rule header and rule option

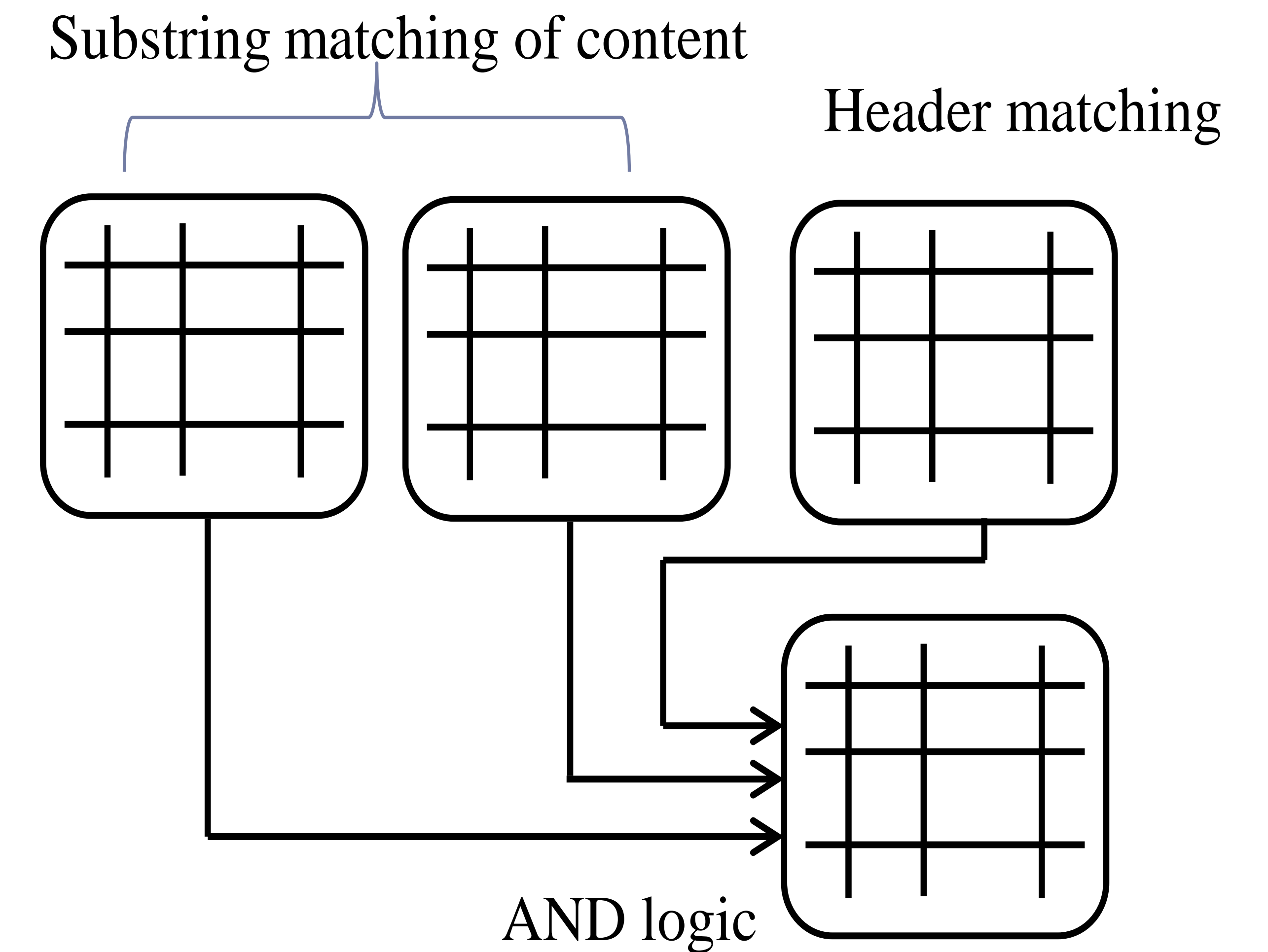
Rule Header :	Rule Options:
Syntax before the parenthesis	Syntax Enclosed within parenthesis

The Memristor

Nanoscale non-volatile device
Large varying resistance range
On Resistance : 125KΩ
Off Resistance : 125MΩ



Architecture for SNORT IDS



Results

System/ Parameter	Memristor crossbar	Intel E8400
<i>Area (mm²)</i>	2.02E-02	107
<i>Power (W)</i>	4.11E-04	65
<i>Throughput(Mbps)</i>	408	15.7

Conclusion

Intrusion Detection is done at
Extreme low power about 150K times power efficiency of Intel processor.
Consumes very little area for desired string matching task
Obtained better throughput than traditional architectures
Can be widely used in power limited devices like mobiles etc.